

VARIABLE FREQUENCY PWM CONTROLLER CIRCUIT

ABSTRACT OF THE DISCLOSURE

The present invention discloses a circuit that generates a variable frequency pulse width modulation (VF PWM) signal. Different from the conventional PWM controller, the frequency and duty cycle of the output PWM signal vary with the error-amplified voltage of the feedback loop simultaneously in this invention. The higher the error-amplified voltage of the feedback loop is, the lower the duty cycle with lower frequency will be. A very low duty cycle PWM signal can be generated stably while its frequency is very low.